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## 教師研究成果資料明細



### 研討會論文

1. 許健(Gene Sheu) 、許健(Gene Sheu) 2009.08.15~2009.08.19  
Dependence of Breakdown Voltage on Drift Length and Linear Doping Gradients in SOI RESURF LDMOS Devices  
2009 ICEMI
2. 郭宇鋒(Yufeng Guo) 、王至剛(Zhigong Wang1) 、許健(Gene Sheu) 、許健(Gene Sheu) 2009.07.23~2008.07.25  
VARIATION OF LATERAL THICKNESSTECHNIQUES IN SOI LATERAL HIGH VOLTAGE DEVICE  
2009 International Conference on Communications, Circuits and Systems(ICCAS2009)
3. 許健(Gene Sheu) 、許健(Gene Sheu) 2009.12.13~2009.12.16  
Combining 2D and 3D Device Simulation for Optimizing LDMOS Design  
2009 IEEE International Conference on Electronics Circuits and Systems
4. (Hsin-Chiang You) 、(Yen-Ling Liu) 、(Shyh-chang Tsaur) 、許健(Gene Sheu) 、許健(Gene Sheu) 2009.03.19~2009.03.20  
Simulation Details for the Electrical Field Distribution and Breakdown Voltage of 0.15 $\mu$ m Thin Film SOI Power Device  
The Electrochemical Society Transaction 2009 (©ISTC CSTIC 2009)
5. 許健(Gene Sheu) 、楊紹明、許愉珊、許健(Gene Sheu)  
2009.05.24~2009.05.25  
A High Performance 80V Smart LDMOS Power Device Based on thin oxide technology  
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6. 楊紹明(Shao-Ming Yang) 、許健(Gene Sheu) 2009.08.16~2009.08.19  
Dependence of Breakdown Voltage on Drift Length and Linear Doping Gradients in SOI RESURF LDMOS Devices  
The International Conference on Electronic Measurement & Instruments (ICEMI)
7. 楊紹明(Shao-Ming Yang) 2010.08.3 ~2010.08.6  
Optimizing NSCR ESD Protection Device for BCD 40V Technology  
第二十一屆超大型積體電路設計暨計算機輔助設計技術研討會